



Sustainable digital technologies, the fuel for a competitive and resilient Europe – CEA symposium

Maison Irène et Frédéric Joliot-Curie, Brussels – July 2nd 2025 from 1.30pm to 7pm (closing cocktail included)

As the environmental impact of digital systems grows, sustainable innovation is becoming a strategic imperative. This event will bring together leading research and industry experts, to showcase how greener tech can reduce energy and critical materials use while strengthening industrial leadership/resilience, as well as policy-makers to inspire the preparation of upcoming legislative and regulatory initiatives on the EU agenda. Discover pioneering research and policy insights. Help shape a European digital future that is both sustainable, globally competitive and desirable.

1.30 – 2.00 PM WELCOME COFFEE & REGISTRATION

2.00 – 2.05 PM OPENING REMARKS

Yves SAMSON, CEA

2.05 – 2.20 PM KEYNOTE

Pierre CHASTANET, Head of Unit Microelectronics & Photonics of DG CONNECT- European Commission

2.20 – 3.20 PM SESSION I: TOWARDS A LESS ENERGY-INTENSIVE DIGITAL TRANSITION

Chair: Sébastien DAUVE, CEO – CEA-Leti

Energy consumption in digital technologies

Piotr SZYMANSKI, Director in charge of Energy, Mobility and Climate – Joint Research Centre (JRC)

ICT energy evolution

Viktor ARVIDSSON, Head of Government Affairs, Innovation and Strategy, France, Belgium, Luxembourg, Algeria and Tunisia – Ericsson

Towards energy-efficient Data centers

Lien HESPEL, Vice President Power Systems Benelux, Power Systems Division, Schneider Electric

Energy efficiency at component & device level

Carlo REITA, CEO – Fondazione Chips-IT

3.20 – 3.50 PM COFFEE BREAK



3.50 – 4.50 PM SESSION 2: TOWARDS A GLOBAL APPROACH FOR SUSTAINABLE DIGITAL TECHNOLOGIES

Chair: **François Terrier**, Deputy Director of Programs – CEA-List

Repurposing Obsolete Smartphones into Circular Electronic Equipment: Sustainability Potential and Limitations

David BOL, Professor, Electronic Circuits and Systems– UC Louvain

Critical Raw Materials (CRM) in Microelectronics: Resource uses, manufacturing, legal requirements, recycling

Otmar DEUBZER, Senior Scientist – Fraunhofer IZM

Digital Twins and Digital Threads: Driving Sustainability in the Electronics Value Chain

Herbert TAUCHER, VP Research and Predevelopment for IC and Electronics – Siemens AG

Circular economy – Maximizing value while minimizing waste

Still under definition – ASML

4.50 – 5.05 PM KEYNOTE

Julie GALLAND, Director of the Technological Research Division–CEA

5.05 – 5.50 PM ROUND TABLE: SUSTAINABLE DIGITAL TECHNOLOGIES TO BOOST EUROPE'S COMPETITIVENESS

Muriel ATTANÉ, Secretary General – European Association of Research and Technology Organisations (EARTO)

Pierre CHASTANET, Head of Unit Microelectronics & Photonics of DG CONNECT– European Commission

Julie GALLAND, Director of the Technological Research division–CEA

Erika GYORVARY, Working Group on Green ECS (European Technol. Platform on Smart Systems Integration, EPOSS) and CSEM

Frédérique LE GREVÈS, President of STMicroelectronics France, s& VP of the European Semiconductor Industry Association (ESIA).

Chair : **Yves Samson**, CEA Technological Research Division

5.50 – 6 PM CLOSING REMARKS

Cyril PIQUEMAL, Deputy Permanent Representative of France to the European Union